

NCE N-Channel Enhancement Mode Power MOSFET

GENERAL FEATURES

• $V_{DS} = 50V, I_D = 0.22A$

 $R_{DS(ON)} < 3\Omega @ V_{GS} = 4.5V$

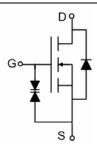
 $R_{DS(ON)}$ < 2 Ω @ V_{GS} =10V

ESD Rating: HBM 2500V

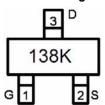
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

Application

- Direct Logic-Level Interface: TTL/CMOS
- Drivers: Relays, Solenoids, Lamps, Hammers, Display, Memories, Transistors, etc.
- Battery Operated Systems
- Solid-State Relays



Schematic diagram



Marking and pin Assignment



SOT-23 top view

Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
138K	BSS138K	SOT-23	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (TA=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	50	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	0.22	Α
Drain Current-Pulsed (Note 1)	I _{DM}	0.88	Α
Maximum Power Dissipation	P _D	0.35	W
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	℃

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2) R _{BJA} 350 C/W		$R_{ heta JA}$	350	°C/W
---	--	----------------	-----	------

Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V_{GS} =0 V I_D =250 μ A	50			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =50V,V _{GS} =0V			1	μA



http://www.ncepower.com

BSS138K

Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V			±10	uA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	0.6	0.9	1.2	V
Drain Source On State Registance	D	V _{GS} =4.5V, I _D =0.2A		1.2	3	Ω
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =0.22A		1.1	2	Ω
Forward Transconductance	g FS	V _{DS} =10V,I _D =0.2A	0.2			S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	\/ -25\/\/ -0\/		30		PF
Output Capacitance	Coss	V_{DS} =25V, V_{GS} =0V, F=1.0MHz		15		PF
Reverse Transfer Capacitance	C _{rss}	F=1.UIVITIZ		6		PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}				5	nS
Turn-on Rise Time	t _r	V _{DD} =30V,I _D =0.22A			5	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{GEN} =6 Ω			60	nS
Turn-Off Fall Time	t _f				35	nS
Total Gate Charge	Qg	V _{DS} =25V,I _D =0.2A,			2.4	nC
Total Gate Charge	Qg	V _{GS} =10V			2.4	l
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =0.22A			1.3	V
Diode Forward Current (Note 2)	Is				0.22	Α

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

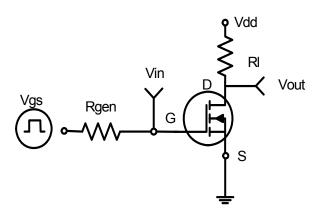


Figure 1:Switching Test Circuit

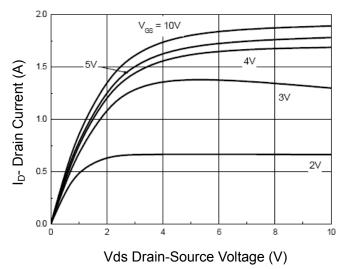


Figure 3 Output CHARACTERISTICS

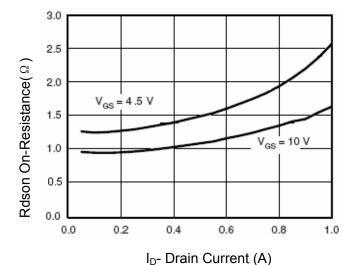


Figure 5 Drain-Source On-Resistance

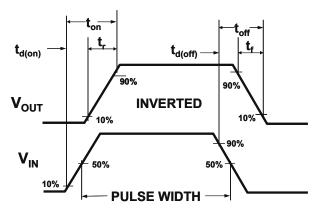


Figure 2:Switching Waveforms

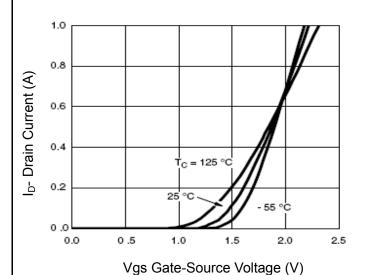
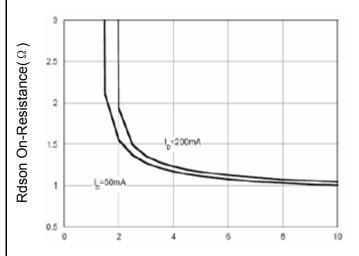
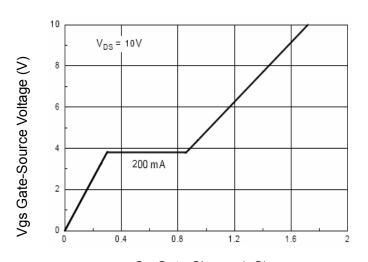


Figure 4 Transfer Characteristics



Vgs Gate-Source Voltage (V) Figure 6 Rdson vs Vgs



Qg Gate Charge (nC) Figure 7 Gate Charge

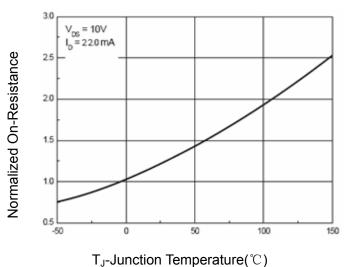
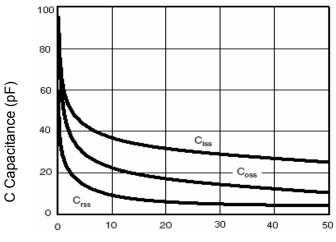
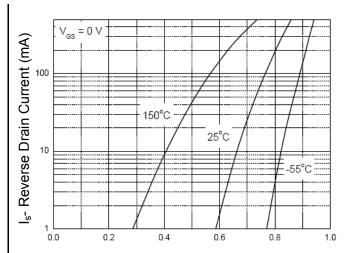


Figure 9 Drain-Source On-Resistance

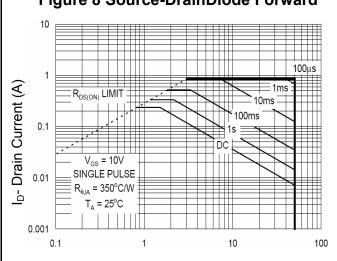


Vds Drain-Source Voltage (V)

Figure 11 Capacitance vs Vds



Vds Source-Drain Voltage (V)
Figure 8 Source-DrainDiode Forward



Vds Drain-Source Voltage (V)

Figure 10 Safe Operation Area

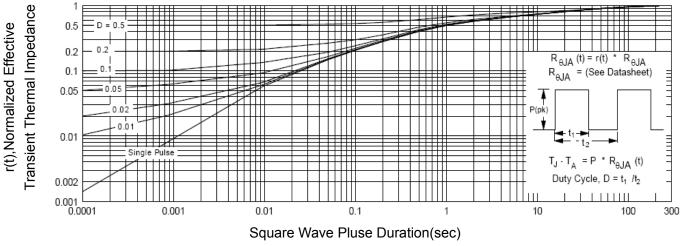
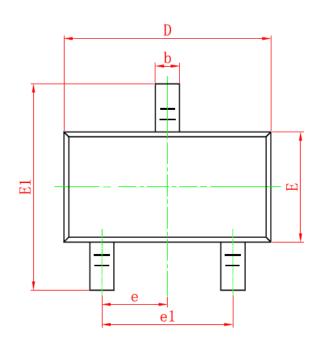
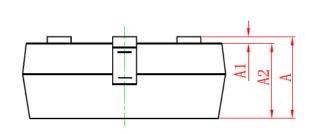


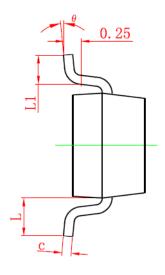
Figure 12 Normalized Maximum Transient Thermal Impedance

SOT-23 PACKAGE INFORMATION

Dimensions in Millimeters (UNIT:mm)







Symbol	Dimensions in Millimeters			
Syllibol	MIN.	MAX.		
Α	0.900	1.150		
A1	0.000	0.100		
A2	0.900	1.050		
b	0.300	0.500		
С	0.080	0.150		
D	2.800	3.000		
E	1.200	1.400		
E1	2.250	2.550		
е	0.950TYP			
e1	1.800	2.000		
L	0.550REF			
L1	0.300	0.500		
θ	0°	8°		

NOTES

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- $5. \ Controlling \ dimension \ is \ millimeter, \ converted \ inch \ dimensions \ are \ not \ necessarily \ exact.$

ATTENTION:

- Any and all NCE power products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your NCE power representative nearest you before using any NCE power products described or contained herein in such applications.
- NCE power assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all NCE power products described or contained herein.
- Specifications of any and all NCE power products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- NCE power Semiconductor CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all NCE power products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of NCE power Semiconductor CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. NCE power believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the NCE power product that you intend to use.
- This catalog provides information as of Sep, 2010. Specifications and information herein are subject to change without notice.